

MMIC

# REFLECTIONLESS FILTER DICE

50Ω DC to 21 GHz

## The Big Deal

- Patented design eliminates in band spurs
- Pass band cut-off up to 21 GHz
- Stop band up to 35 GHz
- Excellent repeatability through IPD\* process
- Unpackaged Die Form



**X-Series**

Available in Low Pass, High Pass and Band Pass designs

## Product Overview

Mini-Circuits' X-Series reflectionless filters employ a novel filter topology which absorbs and terminates stop band signals internally rather than reflecting them back to the source. This new capability enables unique applications for filter circuits beyond those suited to traditional approaches. Traditional filters are reflective in the stop band, sending signals back to the source at 100% of the power level which interact with neighboring components and often result in intermodulation and other interferences. Reflectionless filters eliminate stop band reflections, allowing them to be paired with sensitive devices and used in applications that otherwise require circuits such as isolators, isolation amplifiers or attenuators.

### Key Features

Easy integration with sensitive reflective components, e.g. mixers, multipliers

Enables stable integration of wideband amplifiers

Cascadable

Excellent power handling in a tiny surface mount device

Excellent repeatability of RF performance

Excellent stability over temperature

Operating Temperature up to 105°C

Unpackaged Die form

### Advantages

Reflectionless filters absorb unwanted signals, preventing reflections back to the source. This reduces generation of additional unwanted signals without the need for extra components like attenuators, improving system dynamic range and saving board space.

Because reflectionless filters maintain good impedance in the stop band; they can be integrated with high gain, wideband amplifiers without the risk of creating instabilities in these out of band regions.

Reflectionless filters can be cascaded in multiple sections to provide sharper and higher attenuation, while also preventing any standing waves that could affect pass band signals.

High power handling extends the usability of these filters to the transmit path for inter-stage filtering.

Through semiconductor IPD process, X-series filters are inherently repeatable for large volume production.

With  $\pm 0.3$  dB variation over temperature ideal for use in wide temperature range applications without the need for additional temperature compensation.

Suitable for operation close to high power components

Enables direct integration into customer hybrids

\*IPD – Integrated Passive Device, is a GaAs semiconductor process



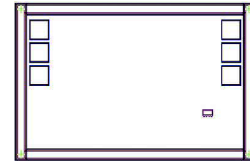
# Reflectionless Low Pass Filter Die

## XLF-123-D+

50Ω DC to 12200 MHz

### Features

- Match to 50Ω in the stop band, eliminates undesired reflections
- Cascadable
- Excellent Power handling
- Protected by US Patent No. 8,392,495



### Applications

- Harmonics Rejection
- Satellite
- Radar
- Military & Space

#### +RoHS Compliant

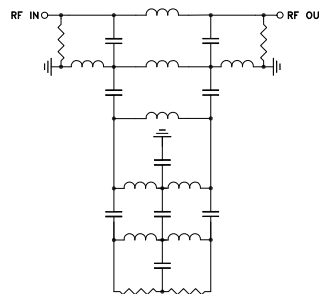
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Ordering Information: Refer to Last Page

### General Description

Mini-Circuits' XLF-123-D+ reflectionless filter die employs a novel filter topology which absorbs and terminates stop band signals internally rather than reflecting them back to the source. This new capability enables unique applications for filter circuits beyond those suited to traditional approaches. Traditional filters are reflective in the stop band, sending signals back to the source at 100% of the power level. These reflections interact with neighboring components and often result in inter-modulation and other interferences. Reflectionless filters eliminate stop band reflections, allowing them to be paired with sensitive devices and used in applications that otherwise require circuits such as isolation amplifiers or attenuators.

### Simplified Schematic



Pad	Description
RF-IN	RF Input Pad
RF-OUT	RF Output Pad
Ground	Ground Bonding Pad

Note: 1. Bond Pad material - Gold  
2. Bottom of Die - Gold plated

**Electrical Specifications<sup>1</sup> at 25°C**

Parameter		F#	Frequency (MHz)	Min.	Typ.	Max.	Unit
Pass Band	Insertion Loss	DC - F1	DC-12200		1.8		dB
	Frequency Cut-off	F2	15000		3.0		dB
	VSWR	DC - F1	DC-12200		1.3		:1
Stop Band	Rejection	F3 - F4	18100 - 19000		16		dB
		F4 - F5	19000 - 29000		20		dB
	VSWR	F3 - F4	18100 - 19000		1.3		:1
		F4 - F5	19000 - 29000		2.5		:1

<sup>1</sup> Measured on Mini-Circuits Characterization test board. Die packaged in 3mm x 3mm, 12-lead MCLP package and soldered on TB-844-123+

**Absolute Maximum Ratings<sup>1,4</sup>**

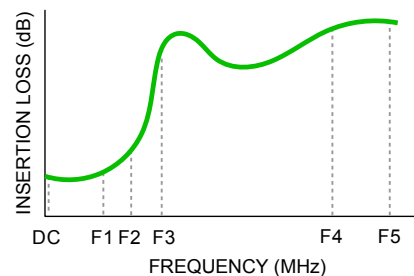
Parameter	Ratings
Operating Temperature	-55°C to +105°C
RF Power Input, Passband (DC-F1) <sup>2</sup>	2W at 25°C
RF Power Input, Stopband (F2-F5) <sup>3</sup>	50mW at 25°C

<sup>2</sup> Passband rating derates linearly to 1W at 105°C ambient

<sup>3</sup> Stopband rating derates linearly to 50mW at 105°C ambient

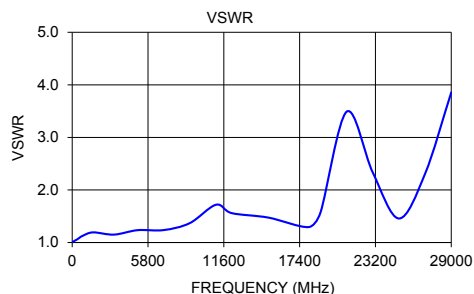
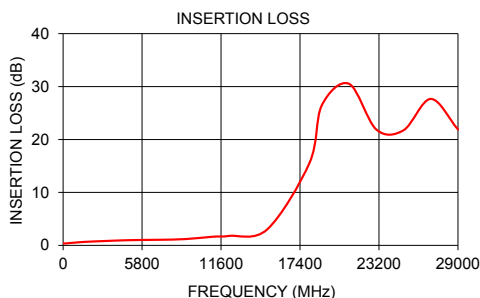
<sup>4</sup> Permanent damage may occur if any of these limits are exceeded.

**Specification Definition**



**Typical Performance Data at 25°C<sup>1</sup>**

Frequency (MHz)	Insertion Loss (dB)	VSWR (:1)
10	0.39	1.01
100	0.35	1.02
200	0.37	1.04
400	0.42	1.06
800	0.50	1.13
1600	0.66	1.19
3200	0.83	1.15
5000	0.99	1.24
7000	1.05	1.24
9000	1.19	1.37
11000	1.63	1.72
12200	1.79	1.56
15000	3.04	1.47
18100	15.60	1.29
19000	26.46	1.57
21000	30.52	3.49
23000	21.89	2.33
25000	21.74	1.46
27000	27.66	2.31
29000	21.90	3.86



**Die Layout**



Fig 1. Die Layout

**Bonding Pad Position**  
(Dimensions in  $\mu\text{m}$ , Typical)

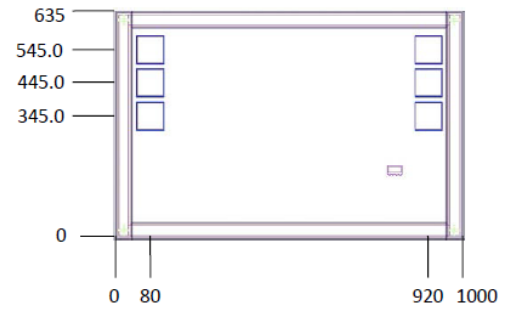


Fig 2. Bonding Pad Positions

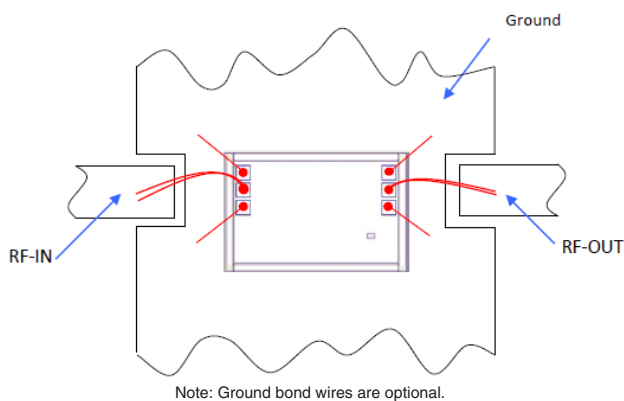
**Critical Dimensions**

Parameter	Values
Die Thickness, $\mu\text{m}$	100
Die Width, $\mu\text{m}$	1000
Die Length, $\mu\text{m}$	635
Bond Pad Size (Ground pad), $\mu\text{m}$	75 x 75

## Assembly and Handling Procedure

1. Storage  
Dice should be stored in a dry nitrogen purged desiccators or equivalent.
2. ESD  
MMIC Gallium Arsenide (GaAs) filter dice are susceptible to electrostatic and mechanical damage. Die are supplied in antistatic protected material, which should be opened in clean room conditions at an appropriately grounded anti-static workstation. Devices need careful handling using correctly designed collets, vacuum pickup tips or sharp antistatic tweezers to deter ESD damage to dice.
3. Die Attach  
The die mounting surface must be clean and flat. Using conductive silver filled epoxy, recommended epoxies are DieMat DM6030Hk-PT/H579/H579 or Ablestik 84-1LMISR4. Apply sufficient epoxy to meet required epoxy bond line thickness, epoxy fillet height and epoxy coverage around total die periphery. Parts shall be cured in a nitrogen filled atmosphere per manufacturer's cure condition. It is recommended to use antistatic die pick up tools only.
4. Wire Bonding  
Bond pad openings in the surface passivation above the bond pads are provided to allow wire bonding to the dice gold bond pads. Thermosonic bonding is used with minimized ultrasonic content. Bond force, time, ultrasonic power and temperature are all critical parameters. Suggested wire is pure gold, 1 mil diameter. Bonds must be made from the bond pads on the die to the package or substrate. All bond wires should be kept as short as low as reasonable to minimize performance degradation due to undesirable series inductance.

## Assembly Diagram



### Recommended Wire Length, Typical

Wire	Wire Length (mm)	Wire Loop Height (mm)
All wires	1.0	0.15

Note: Use double bond wire at RF IN & RF OUT

<b>Additional Detailed Technical Information</b> <i>additional information is available on our dash board.</i>							
<b>Performance Data</b>	Data Table						
	Swept Graphs						
	S-Parameter (S2P Files) Data Set with and without port extension(.zip file)						
<b>Case Style</b>	Die						
<b>Die Ordering and packaging information</b>	<table> <thead> <tr> <th>Quantity, Package</th> <th>Model No.</th> </tr> </thead> <tbody> <tr> <td>Small, Gel - Pak: 10,50,100 KGD*</td> <td>XLF-123-DG+</td> </tr> <tr> <td>Medium†, Partial wafer: KGD*&lt;1900</td> <td>XLF-123-DP+</td> </tr> </tbody> </table> <p>†Available upon request contact sales representative</p> <p>Refer to <a href="#">AN-60-067</a></p>	Quantity, Package	Model No.	Small, Gel - Pak: 10,50,100 KGD*	XLF-123-DG+	Medium†, Partial wafer: KGD*<1900	XLF-123-DP+
Quantity, Package	Model No.						
Small, Gel - Pak: 10,50,100 KGD*	XLF-123-DG+						
Medium†, Partial wafer: KGD*<1900	XLF-123-DP+						
<b>Environmental Ratings</b>	ENV-80						

\*Known Good Dice ("KGD") means that the dice are taken from PCM good wafer and visually inspected according to Mini-Circuits inspection criteria. While this is not definitive, it does help to provide a higher degree of confidence that dice are capable of meeting typical RF electrical parameters specified by Mini-Circuits.

## ESD Rating\*\*

Human Body Model (HBM): Class 1A (250V) in accordance with ANSI/ESD STM 5.1 - 2001

\*\* Tested in industry standard MCLP 3x3mm 12 lead package.

### Additional Notes

- Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
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