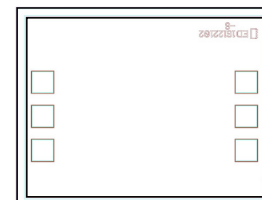


# X2 MMIC Frequency Multiplier Die

## CY2-44-D+

50Ω

Output 14 to 40 GHz



### The Big Deal

- Ultra-wideband, output from 14 to 40 GHz
- Wide input power range, +12 to +18 dBm
- Low conversion loss, 13 dB
- Good fundamental and harmonic suppression:  
F1, 30 dBc; F3, 30 dBc

### Product Overview

Mini-Circuits' CY2-44-D+ is an ultra-wideband MMIC frequency doubler, converting input frequencies from 7 to 20 GHz into output frequencies from 14 to 40 GHz. Its wide output range makes this model suitable for broadband systems as well as a wide variety of narrowband applications. Utilizing GaAs HBT technology, the multiplier offers excellent repeatability.

### Key Features

Feature	Advantages
Broadband, 14 to 40 GHz output	With an output frequency range spanning 14 to 40 GHz, this multiplier supports broadband applications such as defense and instrumentation as well as a wide range of narrowband system requirements.
Low conversion loss, 13 dB typ.	With a low conversion loss, CY2-44-D+ produces higher output power, reducing the need for amplification.
Excellent fundamental and harmonic suppression: <ul style="list-style-type: none"><li>• F1, 30 dBc</li><li>• F3, 30 dBc</li></ul>	Reduces unwanted harmonic signals and the need for additional filtering.
Wide input power range, +12 to +18 dBm	Wide input power signal range accommodates different input signal levels while still maintaining a low conversion loss.
Unpackaged die	Enables the user to integrate the doubler directly into hybrids.

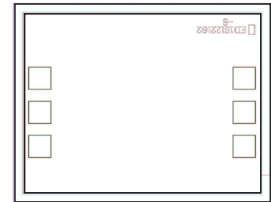
# X2 MMIC Frequency Multiplier Die

## CY2-44-D+

50Ω Output 14 to 40 GHz

### Features

- wideband, output 14 to 40 GHz
- low conversion loss, 13 dB typ.
- high fundamental & harmonic suppression, F1, 30 dBc typ.; F3, 30 dBc typ.



### Applications

- synthesizers
- local oscillators
- 5G

**+RoHS Compliant**  
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

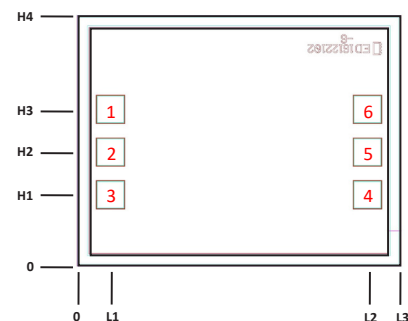
Ordering Information: Refer to Last Page

### Electrical Specifications<sup>1</sup> at 25°C

Parameter	Input Frequency (GHz)	Min.	Typ.	Max.	Unit
Multiplier Factor			2		
Frequency Range, Input (F1)			7 - 20		GHz
Frequency Range, Output (F2)			14 - 40		GHz
Input Power		12	—	18	dBm
Conversion Loss	7 - 16		10.7-16.3		dB
	16 - 20		14.7-19.0		
Harmonic Output <sup>2</sup>	F1	7 - 16	26-48		dBc
	F3	16 - 20	19-28		
	F4	7 - 14.5	24-38		
		7 - 10.8	7-20		

1. Electrical specification are typical measured characteristics on Die using MPI Tiran series 150 μm pitch GSG probe with Pin=+15dBm
2. Harmonics of input frequency below the power level of F2

### Bonding Pad Position



Dimensions in μm, Typical

L1	L2	L3	H1	H2	H3	H4	Thickness	Bond pad size
90.7	986	1085	233.8	383.8	533.8	838	100	100x100

### Maximum Ratings<sup>3</sup>

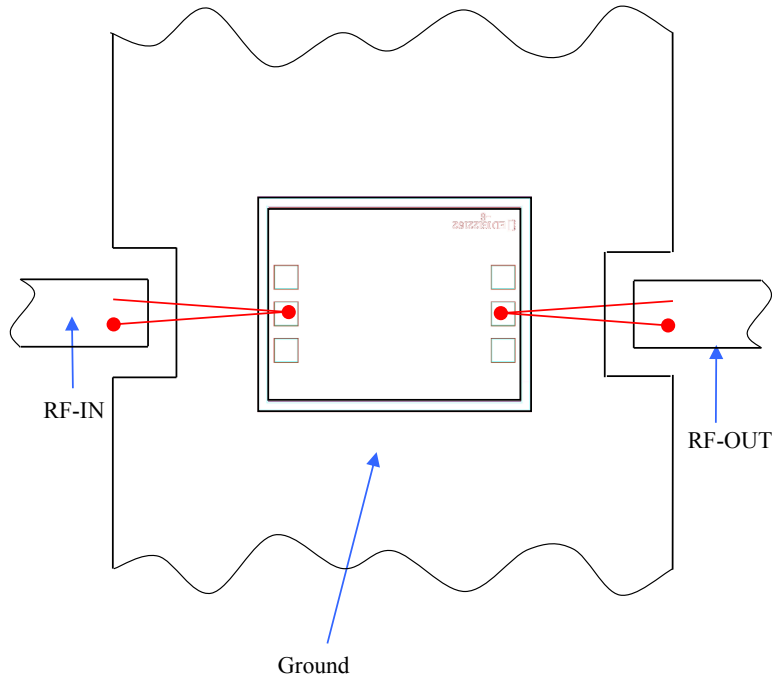
Parameter	Ratings
Operating Temperature	-40°C to 85°C
RF Input Power	21 dBm

3. Permanent damage may occur if any of these limits are exceeded.

Pad#	Function
2	RF-IN
5	RF-OUT
1,3,4,6	Ground
Die Bottom	Ground

- Note: 1. Bond Pad material - Gold
2. Bottom of Die - Gold plated

## Assembly Diagram



Note: Ground bond wires are optional

## Assembly and Handling Procedure

1. Storage  
Dice should be stored in a dry nitrogen purged desiccators or equivalent.
2. ESD  
MMIC doubler dice are susceptible to electrostatic and mechanical damage. Die are supplied in antistatic protected material, which should be opened in clean room conditions at an appropriately grounded anti-static workstation. Devices need careful handling using correctly designed collets, vacuum pickup tips or sharp antistatic tweezers to deter ESD damage to dice.
3. Die Attach  
The die mounting surface must be clean and flat. Using conductive silver filled epoxy, recommended epoxies are DieMat DM6030HK-PT/H579 or Ablestik 84-1LMISR4. Apply sufficient epoxy to meet required epoxy bond line thickness, epoxy fillet height and epoxy coverage around total die periphery. Parts shall be cured in a nitrogen filled atmosphere per manufacturer's cure condition. It is recommended to use antistatic die pick up tools only.
4. Wire Bonding  
Bond pad openings in the surface passivation above the bond pads are provided to allow wire bonding to the dice gold bond pads. Thermosonic bonding is used with minimized ultrasonic content. Bond force, time, ultrasonic power and temperature are all critical parameters. Suggested wire is pure gold, 1 mil diameter. Bonds must be made from the bond pads on the die to the package or substrate. All bond wires should be kept as short as low as reasonable to minimize performance degradation due to undesirable series inductance.

<b>Additional Detailed Technical Information</b> <i>additional information is available on our dash board.</i>									
<b>Performance Data</b>	Data Table								
	Swept Graphs								
	S-Parameter (S2P Files) Data Set with and without port extension(.zip file)								
<b>Case Style</b>	Die								
<b>Die Ordering and packaging information (Note 3)</b>	<table> <thead> <tr> <th>Quantity, Package</th> <th>Model No.</th> </tr> </thead> <tbody> <tr> <td>Small, Gel - Pak: 5,10,50,100 KGD*</td> <td>CY2-44-DG+</td> </tr> <tr> <td>Medium†, Partial Wafer: KGD* &lt;5K</td> <td>CY2-44-DP+</td> </tr> <tr> <td>Large†, Full Wafer</td> <td>CY2-44-DF+</td> </tr> </tbody> </table> <p>†Available upon request contact sales representative</p> <p>Refer to <a href="#">AN-60-067</a></p>	Quantity, Package	Model No.	Small, Gel - Pak: 5,10,50,100 KGD*	CY2-44-DG+	Medium†, Partial Wafer: KGD* <5K	CY2-44-DP+	Large†, Full Wafer	CY2-44-DF+
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Large†, Full Wafer	CY2-44-DF+								
<b>Environmental Ratings</b>	ENV-80								

\*Known Good Dice ("KGD") means that the dice are taken from PCM good wafer and visually inspected in question have been subjected to Mini-Circuits. It does help to provide a higher degree of confidence that dice are capable of meeting typical RF electrical parameters specified by Mini-Circuits.

## ESD Rating\*\*

Human Body Model (HBM): Class 1C (1000 to <2000V) in accordance with ANSI/ESD STM 5.1 - 2001

\*\* Tested in industry standard 3x3 mm, 12-lead MCLP package.

## Additional Notes

- Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
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